

Patent 42478-6200 9-4-83 Hayes

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ejii Takahashi et al.

Serial No.: 10/029,571

Filed: December 20, 2001

For: MULTILAYER BOARD IN WHICH

> WIRING OF SIGNAL LINE THAT REQUIRES TAMPER-RESISTANCE IS COVERED BY COMPONENT OR

FOIL, DESIGN APPARATUS, METHOD, AND PROGRAM FOR THE MULTILAYER BOARD, AND

MEDIUM RECORDING THE

**PROGRAM** 

Patent Examiner: Tsai, Carol S.W.

Group Art Unit: 2857

August 14, 2003

Irvine, California 92614

## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sirs:

In response to the Restriction Requirement of July 25, 2003, please cancel without prejudice Claims 7-29. Please add the newly drafted Claim 30.